

ABSTRACT OF THE DISCLOSURE

A driving circuit of a liquid crystal display device having driver output lines connected to outputs of a data line driver, m pieces of block selection signal lines for sequentially selecting m pieces of blocks, data lines for supplying data to a display area, and a switch for sequentially connecting an i th driver output line to i th, $i+2j$ th, \dots , and $i+2j \times (m-1)$ th data lines in response to signals on the m pieces of block selection signal lines when j is a positive integer smaller than m .